

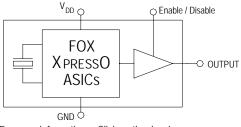
Model: FXO-PC73 SERIES

VPECL 7 x 5mm 3.3V Oscillator

Freq: 0.75 MHz to 1.35GHz

Features

- XTREMELY Low Jitter
- Low Cost
- SPRESS Delivery
- Frequency Resolution to six decimal places
- Stabilities to ± 20 PPM
- -20 to +70°C or -40 to +85°C operating temperatures
- Tri-State Enable / Disable Feature
- Industry Standard Package, Footprint & Pin-Out
- Fully RoHS compliant
- Gold over Nickel Termination Finish
- Serial ID with Comprehensive Traceability



For more information -- Click on the drawing

Description

The Fox XPRESSO Crystal Oscillator is a breakthrough in configurable Frequency Control Solutions. XPRESSO utilizes a family of proprietary ASICs, designed and developed by Fox, with a key focus on noise reduction technologies.

The 3rd order Delta Sigma Modulator reduces noise to the levels that are comparable to traditional Bulk Quartz and SAW oscillators. The ASICs family has ability to select the output type, input voltages, and temperature performance features.

With the XPRESS lead-time, low cost, low noise, wide frequency range, excellent ambient performance, XpressO is an excellent choice over the conventional technologies.

Finished XPRESSO parts are 100% final tested.





nage

Sample

Rev. 12/12/2007

Need a

Applications

- ANY application requiring an oscillator
- SONET
- Ethernet
- Storage Area Network
- Broadband Access
- Microprocessors / DSP / FPGA
- Industrial Controllers
- Test and Measurement Equipment
- Fiber Channel

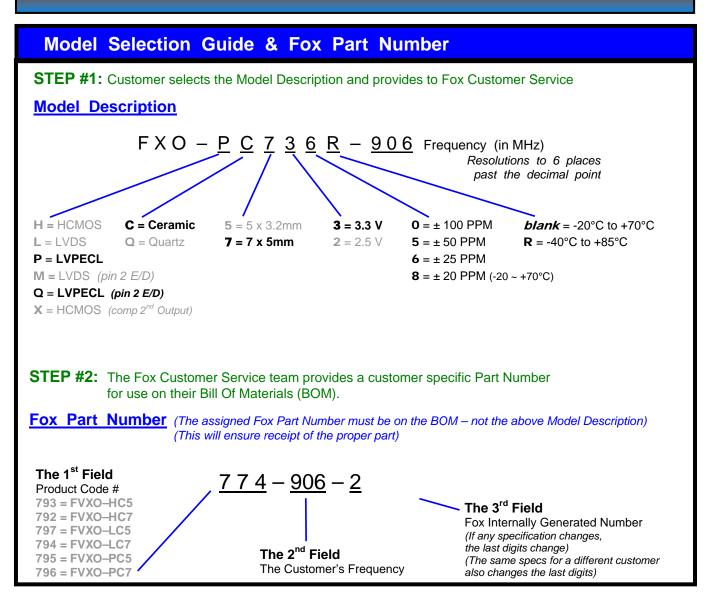
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This example, FXO-PC736R-906 = LVPECL Output, Ceramic, 7 x 5mm Package, 3.3V, ±25 PPM Stability, -40 to +85°C Temperature Range, at 906 MHz



Electrical Characteristics					
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)		
Frequency Range	Fo		0.750 MHz to 1.35 GHz		
Frequency Stability ¹			100, 50, 25, & 20 ppm		
Temperature Range	T _o T _{stg}	Standard operating <i>Optional operating</i> Storage	-20°C to +70°C -40°C to +85°C -55°C to +125°C		
Supply Voltage	V _{DD}	Standard	3.3 V ± 5%		
Input Current (@ Standard Load)	I _{DD}	Standard Load	120 mA		
Output Load	I	Standard	50 Ohms into V_{DD} -2 V_{DC} . TYP.		
Start-Up Time	Ts		10 mS		
Output Enable / Disable Time			100 nS		
Moisture Sensitivity Level	MSL	JEDEC J-STD-20	1		
Termination Finish			Au		

Note 1 – Stability is inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock and vibration.

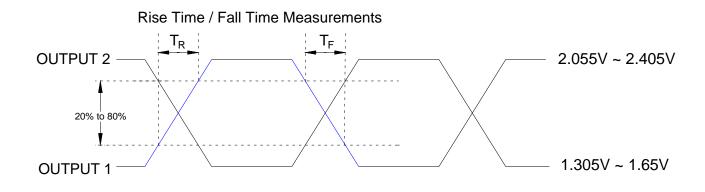
Absolute Maximum Ratings (Useful life may be impaired. For user guidelines only, not tested)					
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)		
Input Voltage	V _{DD}		–0.5V to +5.0V		
Operating Temperature	T _{AMAX}		–55°C to +105°C		
Storage Temperature	T _{STG}		–55°C to +125°C		
Junction Temperature			150°C		
ESD Sensitivity	HBM	Human Body Model	1 kV		

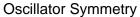




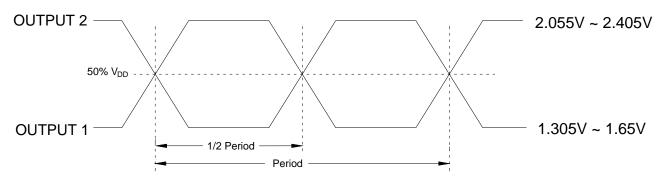
Output Wave Characteristics					
Parameters	Symbol	Condition	Maximum Value (unless otherwise noted)		
Low Output Voltage	V _{OL}	0.75 MHz to 1.35 GHz	1.305V ~ 1.65V		
High Output Voltage	V _{OH}	0.75 MHz to 1.35 GHz	2.055V ~ 2.405V		
Typical Complimentary Difference	V _{P-P}	0.75 MHz to 1.35 GHz	0.750 V _{P-P} Тур		
Output Symmetry (See Drawing Below)		@ 50% V _{P-P} Level	45% ~ 55%		
Output Enable Note1 (PIN # 1) Voltage	V _{IH}		> 70% V _{DD}		
Output Disable ^{Note1} (PIN # 1) Voltage	VIL		< 30% V _{DD}		
Cycle Rise Time (See Drawing Below)	T _R	0.75 MHz to 1.35 GHz	400 pS (20%~80%)		
Cycle Fall Time (See Drawing Below)	T _F	0.75 MHz to 1.35 GHz	400 pS (80%~20%)		

Note1 An optional PIN # 2 as Enable / Disable is available – see Model Selection Guide (page 2)





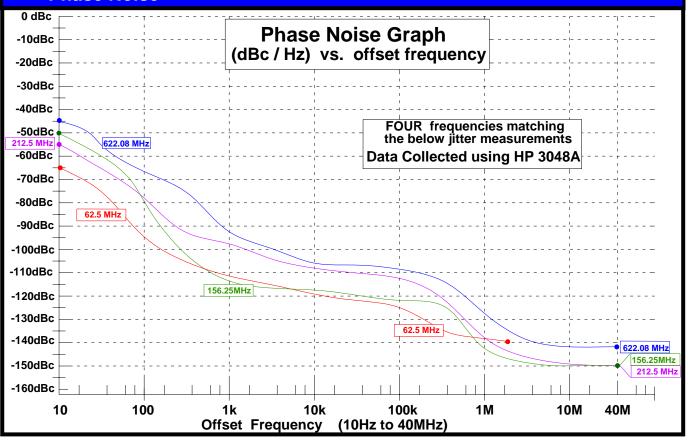
Ideally, Symmetry should be 50/50 for 1/2 period -- Other expressions are 45/55 or 55/45







Phase Noise



Jitter is frequency dependent. Below are typical values at select frequencies.

LVPECL Phase Jitter & Time Interval Error (TIE)				
Frequency	Phase Jitter (12kHz to 20MHz)	TIE (Sigma of Jitter Distribution)	Units	
62.5 MHz	1.01	3.1	pS RMS	
156.25 MHz	0.86	3.5	pS RMS	
212.5 MHz	1.05	3.6	pS RMS	
622.08MHz	0.94	3.5	pS RMS	

Phase Jitter is integrated from HP3048 Phase Noise Measurement System; measured directly into 50 ohm input; V_{DD} = 3.3V. <u>TIE</u> was measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software; V_{DD} = 3.3V. *Per MJSQ spec (Methodologies for Jitter and Signal Quality specifications)*

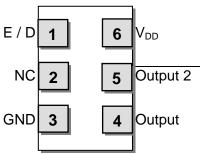
LVPECL Random & Deterministic Jitter Composition					
Random (Rj)Deterministic (Dj)Total Jitter (Tj)Frequency(pS RMS)(pS P-P)(14 x Rj) + Dj					
62.5 MHz	1.27	8.1	26.2 pS		
156.25 MHz	1.29	9.3	27.7 pS		
212.5 MHz	1.22	8.6	26.1 pS		
622.08 MHz	1.21	9.6	26.8 pS		

<u>**Ri and Di**</u>, measured on LeCroy LC684 Digital Storage Scope, directly into 50 ohm input, with Amherst M1 software. Per **MJSQ** spec (Methodologies for Jitter and Signal Quality specifications)

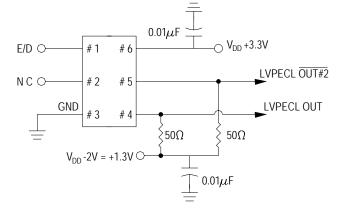




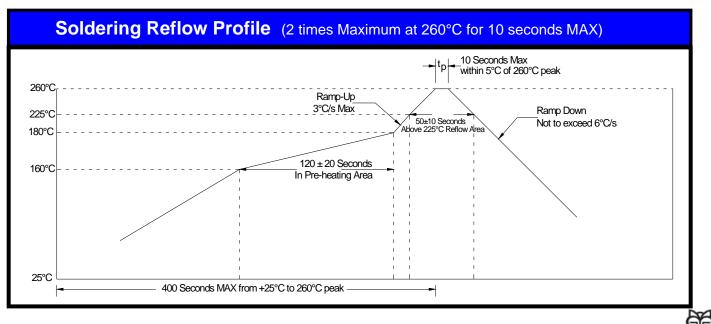
Pin	Pin Description and Recommended Circuit			
Pin #	Name	Туре	Function	
1	E/D ¹	Logic	Enable / Disable Control of Output (0 = Disabled)	
2	NC ²		No Connection – Leave OPEN	
3	GND	Ground	Electrical Ground for V _{DD}	
4	Output	Output	Output LVPECL Oscillator Output	
5	Output 2	Output Complimentary LVPECL Output		
6	6 V _{DD} ³ Power Power Supply Source Voltage			
NOTES: ¹ Includes pull-up resistor to V _{DD} to provide output when the pin (1) is No Connect. (Also see note 2) ² An optional pin # 2 Enable / Disable is available. ³ Installation should include a 0.01μF bypass capacitor placed between V _{DD} (Pin 6) and GND (Pin 3) to minimize power supply line noise.				



Terminations as viewed from the Top NOTE: XPRESSO LVPECL XOs are designed to fit on Industry Standard, 6 pad layouts

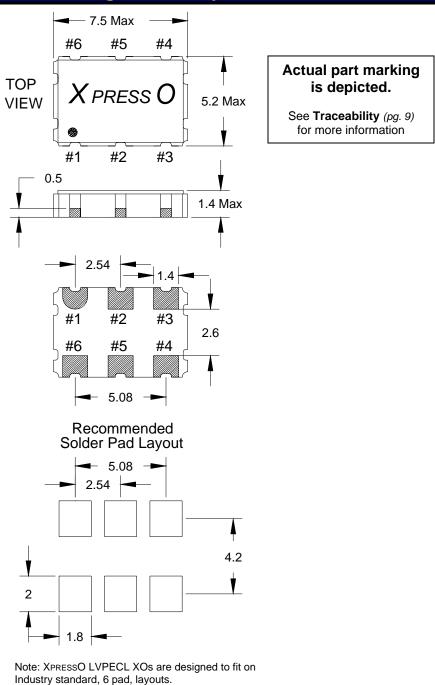


Enable / Disable Control	
Pin # 1 (state)	Output (Pin # 4, Pin # 5)
OPEN (No Connection)	ACTIVE Output
"1" Level V _{IH} > 70% V _{DD}	ACTIVE Output
"0" Level V _{IL} < 30% V _{DD}	High Impedance





Mechanical Dimensional Drawing & Pad Layout



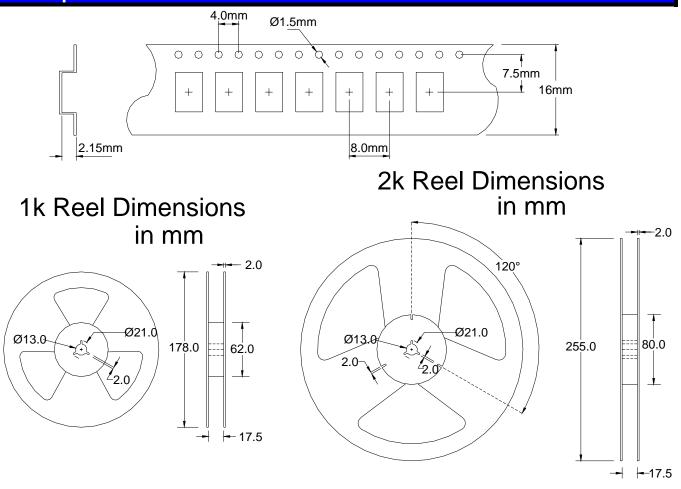
	Pin Connections		
#1)	E/D	#4)	Output
#2)	NC	#5)	Output 2
#3)	GN	D #6)	V _{DD}

Drawing is for reference to critical specifications defined by size measurements. Certain non-critical visual attributes, such as side castellations, reference pin shape, etc. may vary

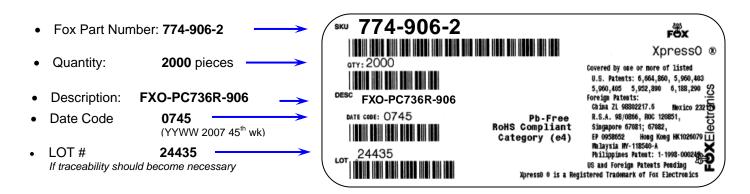




Tape and Reel Dimensions



Labeling (Reels and smaller packaging are labeled with the below)



An additional identification code is contained internally if tracking should ever be necessary





Traceability – LOT Number & Serial Identification

LOT Number

The LOT Number has direct ties to the customer purchase order. The LOT Number is marked on the "Reel" label, and also stored internally on non-volatile memory inside the XPRESSO part. XPRESSO parts that are shipped Tape and Reel, are also placed in an Electro Static Discharge (ESD) bag and will have the LOT Number labeled on the exterior of the ESD bag.

It is recommended that the XPRESSO parts remain in this ESD bag during storage for protection and identification.

If the parts become separated from the label showing the LOT Number, it can be retrieved from inside one of the parts, and the information that can be obtained is listed below:

- Customer Purchase Order Number
- Internal Fox Sales Order Number
- Dates that the XPRESSO part was shipped from the factory
- The assigned customer part number
- The specification that the part was designed for

Serial Identification

The Serial ID is the individualized information about the configuration of that particular XPRESSO part. The Serial ID is unique for each and every XPRESSO part, and can be read by special Fox equipment.

With the Serial ID, the below information can be obtained about that individual, XPRESSO part:

- Equipment that the XPRESSO part was configured on
- Raw material used to configure the XPRESSO part
- Traceability of the raw material back to the foundries manufacturing lot
- Date and Time that the part was configured
- Any optimized electrical parameters based on customer specifications
- Electrical testing of the actual completed part
- Human resource that was monitoring the configuration of the part

Fox has equipment placed at key Fox locations World Wide to read the Lot Identification and Serial Number of any XPRESSO part produced and can then obtain the information from above within 24 hours





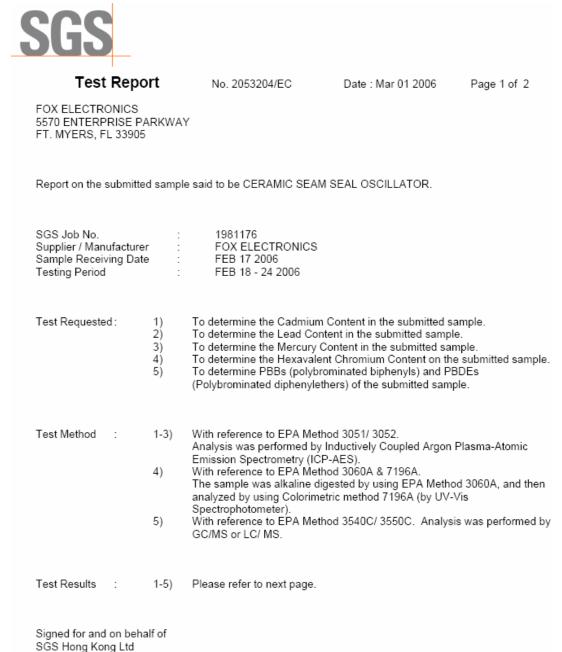
RoHS Material Declaration

	Material Name	Component	Content	Content	
			(mg)	(w t %)	(CAS Number)
Cover	Kovar	Nickel (Ni)	5.09	3.63%	7440-02-0
		Cobalt (Co)	3.15	2.24%	7440-48-4
		Iron (Fe)	9.47	6.75%	7439-89-6
Base	Ceramic	Alumina (Al ₂ O ₃)	79.178	56.4%	1344-28-1
		Silicon Oxide (SiO ₂)	3.143	2.24%	14808-60-7
		Chromium Oxide (Cr ₂ O ₃)	3.379	2.41%	1308-38-9
		Titanium Oxide (TiO ₂)	0.873	0.622%	13463-67-7
		Magnesium Oxide (MgO)	0.437	0.311%	1309-48-4
		Calcium Oxide (CaO)	0.297	0.212%	1305-78-8
	+ Metallization	Tungsten (W)	12.272	8.74%	7440-33-7
		Molybdenum (Mo)	0.380	0.27%	7439-98-7
	+ Nickel Plating	Nickel (Ni)	4.740	3.38%	7440-02-0
		Cobalt (Co)	0.395	0.28%	7440-48-4
	+ Gold Plating	Gold (Au)	0.624	0.445%	7440-57-5
	+Seal ring	Iron (Fe)	5.809	4.14%	7439-89-6
		Nickel (Ni)	3.119	2.22%	7440-02-0
		Cobalt (Co)	1.829	1.30%	7440-48-4
	+silver solder	Silver (Ag)	2.269	1.62%	7440-22-4
		Copper (Cu)	0.400	0.285%	7440-50-8
ΙC	I C	Aluminum (Al)	0.0021	0.00150%	7429-90-5
		Silicon (Si)	0.950	0.68%	7440-21-3
	Gold	Gold (Au)	0.480	0.342%	7440-57-5
	Adhesive	Silver (Ag)	0.000210	0.000150%	7440-22-4
		Ероху	0.0000700	0.0000499%	
Crystal	Crystal	Silicon Dioxide (SiO ₂)	2.04	1.45%	14808-60-7
	Electrode	Silver (Ag)	0.019	0.0135%	7440-22-4
		Nickel (Ni)	0.000159	0.000113%	7440-02-0
	Adhesive	Silver (Ag)	0.00037	0.000264%	7440-22-4
		Silicon (Si)	0.000125	0.000089%	7440-21-3
TOTAL			140.3	100.00%	





3rd Party (SGS) Material Report



Ho Ting, Family Laboratory Executive

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Party (SGS) Material Report (continued) 3^{rc}



Test Report

:

No. 2053204/EC

Date : Mar 01 2006

Page 2 of 2

Test Results

Test Item	<u>1</u>	Detection Limit
1) Cadmium (Cd)	ND	2 ppm
2) Lead (Pb)	ND	2 ppm
3) Mercury (Hg)	ND	2 ppm
 Hexavalent Chromium (Cr⁶⁺) 	ND	2 ppm

(Results shown are of the total weight of samples)

Note : ppm = mg/kg

ND = Not Detected Not detected is reported when the reading is less than detection limit value

_5)		
Flame Retardants	1	Detection Limit
Polybrominated Biphenyls (PBBs)		
Monobromobiphenyl	ND	5 ppm
Dibromobiphenyl	ND	5 ppm
Tribromobiphenyl	ND	5 ppm
Tetrabromobiphenyl	ND	5 ppm
Pentabromobiphenyl	ND	5 ppm
Hexabromobiphenyl	ND	5 ppm
Heptabromobiphenyl	ND	5 ppm
Octabromobiphenyl	ND	5 ppm
Nonabromobiphenyl	ND	5 ppm
Decabromobiphenyl	ND	5 ppm
Polybrominated Diphenylethers (PBDEs)		
Monobromodiphenyl ether	ND	5 ppm
Dibromodiphenyl ether	ND	5 ppm
Tribromodiphenyl ether	ND	5 ppm
Tetrabromodiphenyl ether	ND	5 ppm
Pentabromodiphenyl ether	ND	5 ppm
Hexabromodiphenyl ether	ND	5 ppm
Heptabromodiphenyl ether	ND	5 ppm
Octabromodiphenyl ether	ND	5 ppm
Nonabromodiphenyl ether	ND	5 ppm
Decabromodiphenyl ether	ND	5 ppm

Note ppm = mg/kg

ND = Not Detected

Not detected is reported when the reading is less than detection limit value.

Sample Description: 1. Black Ceramic w/ Silvery, Golden Metal w/ Silvery Chips

*** End of Report ***

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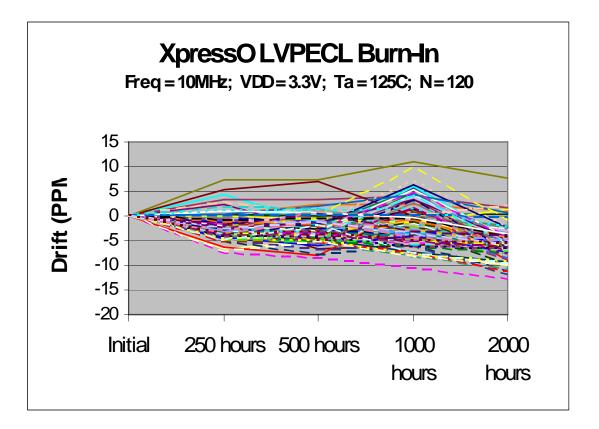


Mechanical Testing

Parameter	Test Method
Mechanical Shock	Drop from 75cm to hardwood surface – 3 times
Mechanical Vibration	10~55Hz, 1.5mm amplitude, 1 Minute Sweep 2 Hours each in 3 Directions (X, Y, Z)
High Temperature Burn-in	Under Power @ 125°C for 2000 Hours (results below)
Hermetic Seal	He pressure: 4 \pm 1 kgf / cm ² 2 Hour soak

2,000 Hour Burn-In

Burn-In Testing – under power 2000 Hours, 125°C







MTTF / FITS Calculations

Products are grouped together by process for MTTF calculations. (All XpressO output and package types are manufactured with the same process)

Number of Parts Tested:
Number of Failures:360 (120 of each output type: HCMOS, LVDS, LVPECL)Number of Failures:
Test Temperature:125°CNumber of Hours:
20002000

MTTF was calculated using the following formulas:

[1.] Device Hours (devhrs) = (number of devices) x (hours at elevated temperature in °K)

 $[2.] MTTF = \frac{devhrs \times af \times 2}{\chi^2}$

[3.] FITS = $\frac{1}{MTTF}$ * 10⁹

Where:

Label	Name	Formula/Value
af	Acceleration Factor	$\boldsymbol{\ell}^{(\frac{eV}{k})\times(\frac{1}{t_1}-\frac{1}{t_2})}$
eV	Activation Energy	0.40 V
k	Bolzman's Constant	8.62 X 10⁻⁵ <i>eV</i> /ºK
t ₁		Operating Temperature (°K)
t ₂		Accelerated Temperature (°K)
Θ	Theta	Confidence Level (60% industry standard)
r	Failures	Number of failed devices
X ²	Chi-Square	statistical significance for bivariate tabular analysis [table look- up] based on assumed Θ (Theta – confidence) and number of failures (r) For zero failures (60% Confidence): $\chi^2 = 1.830$

DEVICE-HOURS = 360 x 2000 HOURS = 720,000

ACCELERATION FACTOR = $e^{(\frac{0.40}{8.625}) \times (\frac{1}{298} - \frac{1}{398})} = 49.91009$

MTTF = $\frac{720,000 \times 49.91009 \times 2}{1.833}$ = 15,607,065 Hours

Failure Rate = $\frac{1.833}{720,000 \times 49.91009 \times 2}$ = 6.41E-8

FITS = Failure Rate *1E9 = 64





Notes :

Other XPRESSO Links			
XPRESSO Brochure			
Crystal Oscillators			
HCMOS 5 x 3.2mm 3.3V XO 0.75 to 250MHz			
HCMOS 7 x 5mm 3.3V XO 0.75 to 250MHz			
LVPECL 5 x 3.2mm 3.3V XO 0.75 to 1.35GHz			
LVPECL 7 x 5mm 3.3V XO 0.75 to 1.35GHz			
LVDS 5 x 3.2mm 3.3V XO 0.75 to 1.35GHz			
LVDS 7 x 5mm 3.3V XO 0.75 to 1.35GHz			
Voltage Controlled Crystal Oscillators			
HCMOS 5 x 3.2mm 3.3V VCXO 0.75 to 250MHz			
HCMOS 7 x 5mm 3.3V VCXO 0.75 to 250MHz			
LVPECL 5 x 3.2mm 3.3V VCXO 0.75 to 1.35GHz			
LVPECL 7 x 5mm 3.3V VCXO 0.75 to 1.35GHz			
LVDS 5 x 3.2mm 3.3V VCXO 0.75 to 1.35GHz			
LVDS 7 x 5mm 3.3V VCXO 0.75 to 1.35GHz			
Main Website, www.foxonline.com			

Main Website www.foxonline.com

Patent Numbers: US 6,664,860, US 5,960,403, US 5,952,890; US 5,960,405; US 6,188,290; Foreign Patents: R.S.A. 98/0866, R.O.C. 120851; Singapore 67081, 67082; EP 0958652 China ZL 98802217.6, Malaysia MY-118540-A, Philippines 1-1998-000245, Hong Kong #HK1026079, Mexico #232179 US and Foreign Patents Pending XpressO™ Fox Electronics

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The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

